

	W.				ages Title	Current OR	Current XRef Retrieval Clas	inventor .
5			US 6018229 A	20000125	Lithium-ion battery pack with integral switching regulator using cutoff	320/112	320/114; 320/140	Mitchell, Nathan et al.
6			US 6015534 A	20000118	PCR sample tube	422/102	215/306; 220/810;	Alwood; John Girdner
7		1	US 6008126 A	19991228	Membrane dielectric isolation IC fabrication	438/667	257/E21.545; 257/E21.564;	Leedy; Glenn Joseph
8			US 5999849 A	19991207	Low power rectifier circuit for implantable medical device	607 <i>1</i> 2	327/104; 363/127	Gord; John C. et al.
9	1.		US 5999848 A	19991207	Dalay chainable sensors and stimulators for implantation in living tissue	607/2	607/60	Gord: John C. et al.
0			US 5998805 A	19991207	Active matrix OED array with improved OED cathode	257/40	257/103; 257/59;	Shi: Song Q. et al.
11		$\{7,3\}$	US 5994713 A	19991130	Filmless photon imaging apparatus	250/591	250/370.09; 257/E27.146	Becker, Charles D. et al.
2	Þ	i i	US 5987987 A	19991123	Angular velocity sensor, related method for manufacturing the sensor, and	73/504,16	310/316.01; 310/368;	Watarai; Takehiro
3	₽.		US 5986505 A	19991116	Circular stripline package incorporating a MMIC low noise amplifier	330/286	330/307	Torgeson, George P. et al.
и.	P.	r	US 5986278 A	19991116	Filmless photon imaging apparatus	250/580	250/370.09; 257/E27.148	Becker, Charles D. et al.
35			US 5985693 A	19991116	High density three-dimensional IC interconnection	438/107	257/E21.545; 257/E21.573;	Leedy, Glenn Joseph
16	Þ	r	US 5982038 A	19991109	Cast metal seal for semiconductor substrates	257/772	257/779; 257/E23.193	Toy; Hilton T, et al.
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
[1]	O.	(board or substrate) and capacitors and (mermetic ad) seal)	US-PGPUB; USPAT; USOGR; EPO; JPO; DERWENT	OR	ON	2006/01/16:16:53
L2	1359	(board or substrate) and capacitors and (hermetic adj seal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/16 16:45
L3	784	(board or substrate) and capacitors and (hermetic adj seal) and conductor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/16:16:45
L4	520	(board or substrate) and capacitors and (hermetic adj seal) and conductor and ceramic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/16 16:46
L5	212	(board or substrate) and capacitors and (hermetic adj seal) and conductor and ceramic and flexible	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/16 16:50
L6	2	"20030191505"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/16 16:50
L7.	0	(board or substrate) with (mermetic adj seal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/16 16:53
L8	1620	(board or substrate) with (hermetic adj seal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/16 16:53
<u>.</u>	24	(board or substrate) with (hermetic adj. seal) with capacitors	US-PGPUB; USPAT; USOCR; EPO: JPO; DERWENT	OR	ON	2006/01/16:16:53

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L10	29	("2756375" "3235939"	US-PGPUB;	OR	ON	2006/01/16 16:56
		"3617830" "3920888"	USPAT;	ľ		
1		"4083022" "4148003"	USOCR	j		
İ		"4152540" "4220813"				
İ		"4247881" "4314213"	ľ		1	
		"4352951" "4362792"				
		" 44 21947" " 44 24551"	ľ	l		
		"4456786" "4500159"		i		
	•	"4737601" "4741710"				
		"5032692" j "5070605" j				
		"5333095" "5 4 06444"				
		"5531003" i "5620476" i			i	
İ		"5650759" "5735884"				
		"5759197").PN.				
LII	31	("5650759"):URPN.	USPAT	OR	ON	2006/01/16 16:57

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